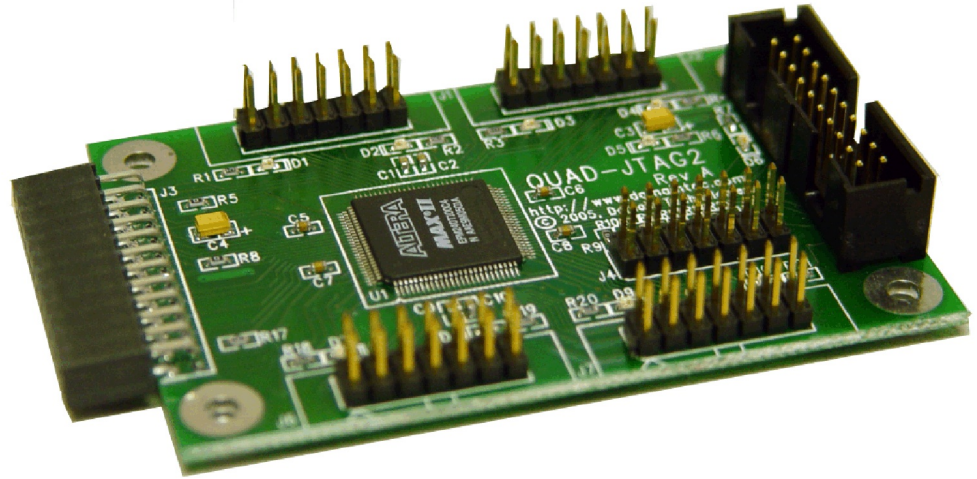


ZSP-QUAD-JTAG Adapter

Universal, programmable, multi-JTAG adapter

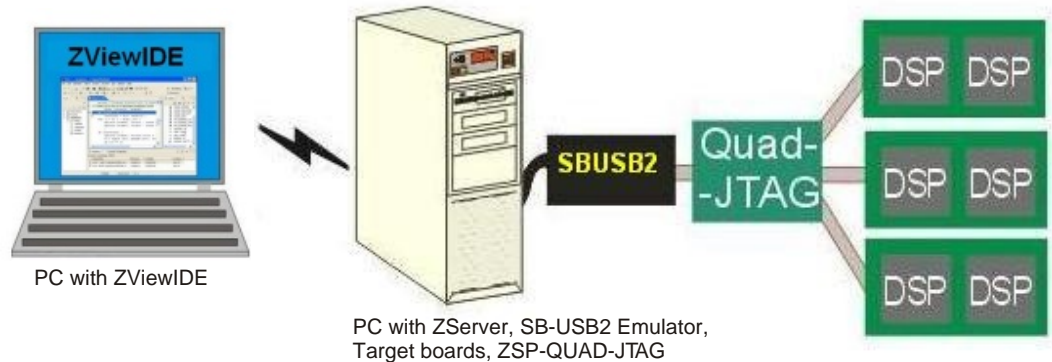


ZSP-QUAD-JTAG Features:

- 20 pin ARM cable
- 2 14x14 ribbon cables
- 2 14x10 ribbon cables featuring separate single pin and reset control connector

Overview:

Efficiently debug multiple daisy-chained devices using the ZSP-QUAD-JTAG adapter. This universal, programmable, multi-JTAG adapter for the SB-USB2-ZSP and ZSP-USB-JTAG emulators enables simultaneous control and interaction of multiple devices through their debug sessions. The board allows daisy-chaining of up to four 10/14-pin JTAG headers or 3 10/14-pin JTAG headers and one 20-pin ARM header. Each connected board's DSP can be controlled synchronously as well as individually for the utmost flexibility in a testing environment.



Product Description:

The ZSP-QUAD-JTAG adapter is used in conjunction with VeriSilicon's ZViewIDE Debugger and ZServer Connectivity software for simultaneous control of multiple devices. Daisy-chain up to four 10/14-pin JTAG headers and one 20-pin ARM header. In the example above, an engineer is debugging multiple target boards that are daisy chained together using the ZSP-QUAD-JTAG adapter and Domain's SB-USB2-ZSP emulator. A ZViewIDE debug session is enabled for each daisy chained device. Remote access to the ZViewIDE sessions is provided by ZServer connectivity software. Engineers can run/halt/single step the devices synchronously as well as individually.

Testing and Debugging Tools

ZSP-USB-JTAG, SB-USB2-ZSP Probe Features:

- Powered by USB port
- JTAG interface
- Integrates with:
 - ZView IDE
 - ZSP Console GDB



ZSP-USB-JTAG: JTAG Probe for ZSP Debugging

Both compact and portable, the ZSP-USB-JTAG emulator is designed to support software development on the VeriSilicon ZSP cores via a host PC USB port. In addition, the emulator supports multi-core and multi-user debugging on a network over a TCP/IP connection, as well as JTAG boundary scan operations such as testing and flash memory programming, with available software. The ZSP-

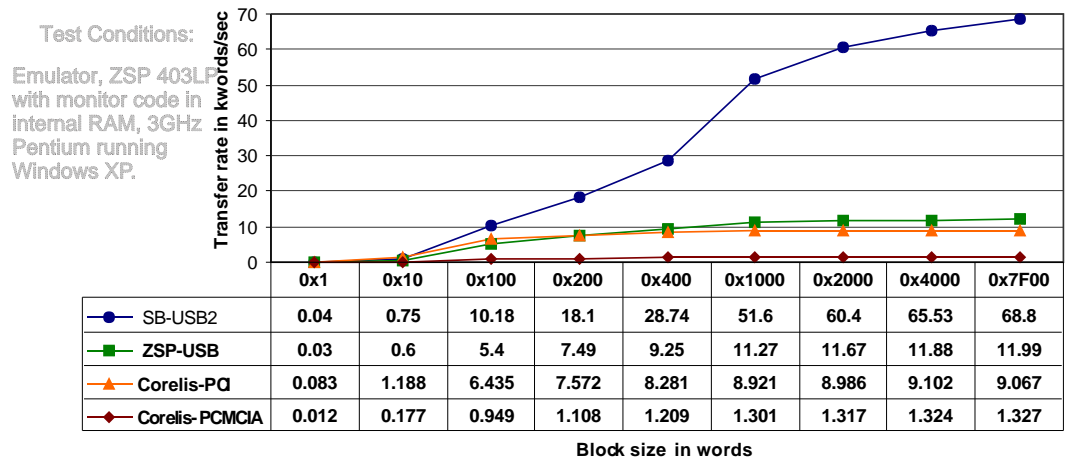
USB-JTAG emulator is powered from the USB connection, which eliminates the need for a separate power supply and also making it ideal for use with notebook computers.



SB-USB2-ZSP: Fast JTAG Probe for ZSP Debugging

The SB-USB2-ZSP emulator enables efficient and productive embedded software debugging. This compact and portable probe utilizes the JTAG interfaces for debug and in-circuit testing supplied with the VeriSilicon ZSP cores. Unlimited software breakpoints

within all ranges of program memory implemented in RAM are supported. Upload and download rates are up to 5 times faster than comparable products:



Package Contents:

- SB-USB2-ZSP Emulator
- ZSP-QUAD-JTAG Adapter
- Signum System's Chameleon Debugger

*Requires VeriSilicon's ZViewIDE

ZSP/ARM Development Package: Debug Multiple CPUs on a Single JTAG Scan Chain

The ZSP/ARM development system is your interface to embedded project development from CPU evaluation to production testing. Support different CPUs and DSPs (including ARM and ZSP) in plain or multi-core(SoC) targets through one package!

This multi-core development environment provides a debug interface running on a PC host connected to an emulator, which communicates with the ZSP/ARM targets. The package provides Signum System's Chameleon Debugger for ARM and Domain



Technologies: SB-USB2-ZSP emulator and ZSP-QUAD-JTAG adapter. It utilizes VeriSilicon's ZViewIDE and ZServer connectivity software. This multi-core/device development capability allows the user to start/stop/single-step selected devices simultaneously and independently. Access to multiple devices is provided from single or multiple workstations via TCP/IP connection.