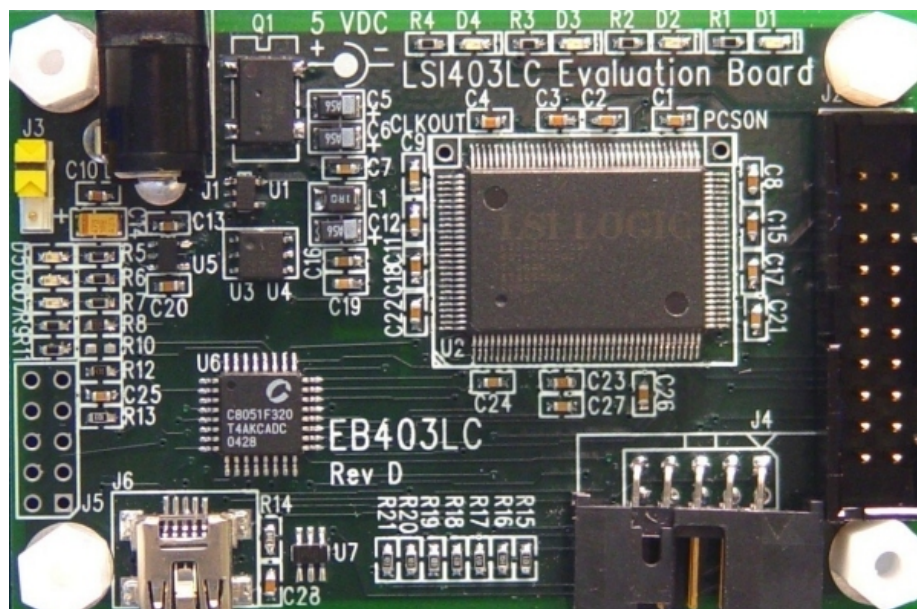


EB403LC- Evaluation Board

Featuring: ZSP 403LC chip

Key Features:

- ZSP 403LC chip
- 20-pin serial port with SPORT pinout
- 4 LED outputs controlled via PIO
- Integrated USB controller with JTAG, 8-bit HPI
- 2 status LEDs for USB controller
- JTAG headers for external JTAG emulator



Overview:

Improve the efficiency of your embedded software development for the ZSP400 core with the EB403LC. This compact evaluation board provides a low cost alternative to the VeriSilicon ZSP 403LP evaluation board.

Product Description:

The EB403LC has an integrated JTAG emulator as well as a host port interface. It is 3 inches by 2 inches in size and designed around the ZSP 403LC chip (128-pin PQFP). The EB403LC has 4 individual LEDs controlled via PIO, which can be used for status information or debugging. An integrated USB controller with 8-bit HPI interface is on board, making it possible to load programs and start/stop the 403LC by simply plugging a USB cable into the board. In fact, the board may be powered via the USB port if the power draw is less than 300mA.

The EB403LC ships with test software including source files. For audio testing, users can quickly show modulated wave, scroll the LEDs and print text to the terminal output.

Supplemental Hardware:

This versatile evaluation board can be integrated with additional hardware from Domain Technologies to support audio and telephony testing:

- Create a 2 or 4 line VOIP demo system by adding the 1 or more DTSLIC2L boards

- Create a 2 channel audio test system with stereo audio input and output by adding the DT4218DB

Package Contents:

- Evaluation Board
- USB cable
- User's Guide with Application Notes

Domain Technologies, Inc.
811 East Plano Pkwy, Suite 115
Plano, TX 75074
Tel.: 972-578-1121
Fax: 972-578-1086
E-mail: info@domaintec.com
Web: <http://www.domaintec.com>



Testing and Debugging

DT4218DB Features:

- 16-bit Stereo Audio Codec
- Delta-Sigma A/D and D/A converters
- Stereo Line Out
- Stereo Line In
- Dual MIC In

Sampling Rates:

- 48 kHz - 8 kHz
- 44.1 kHz - 7.35 kHz



Audio Test System:
EB403LC, DT4218DB

DT4218DB Audio Adapter

Expand the EB403LC with the DT40218DB audio adapter daughter board. It provides stereo 16-bit codec with sampling rates between 48kHz and 7.32 kHz including 44.1 kHz. The board features stereo line in and line out as well as two independent microphone inputs with preamp and bias voltage for electret microphones.

Utilize the demo software, including sources, to test the CS4218 chip on the DT4218DB audio daughterboard. The Graphical user interface of the demo monitors incoming waveforms and displays their amplitude values.

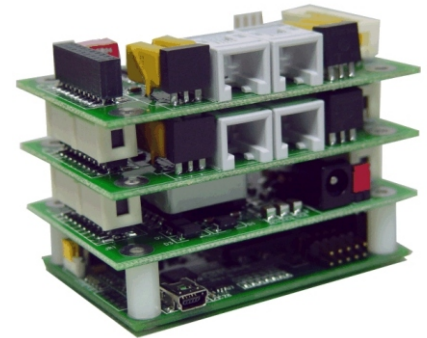
DTSLIC2L Features:

- Dimensions: 3.2" x 2.5"
- 2 Powered Analog Line Interface Jacks
- 2 Independent CODECs
- DTMF, Tone Generation, Zsynth, and Line Interface
- Si3220 chip and two Si3200 chips

DTSLIC2L Telephony Adapter

Expand the EB403LC with the dual subscriber line interface circuit daughterboard. Each DTSLIC2L provides a complete dual analog phone line. Up to four DTSLIC2L boards can be powered by one DTSLICPWR (power supply) board.

Utilize the demo software, including sources, to test the telephony daughterboard. The demo illustrates how to communicate with the DTSLIC2L from the PC through the ZSP host port.



4 Line Test System:
EB403LC, DTSLICPWR, (2)DTSLIC2L

DTSLICPWR Features:

- Power: Coiltronics VP5-0155 and LTC 3704

SB-USB2-ZSP Probe Features:

- Powered by USB port
- JTAG interface
- Integrates with:
 - ZView IDE
 - ZSP Console GDB



SB-USB2-ZSP: Fast JTAG Probe for ZSP Debugging

The SB-USB2-ZSP emulator enables efficient and productive embedded software debugging. This compact and portable probe utilizes the JTAG interfaces for debug and in-circuit testing supplied with the VeriSilicon ZSP cores. Unlimited software breakpoints

within all ranges of program memory implemented in RAM are supported. Upload and download rates are up to 5 times faster than comparable products:

