

DTSTXEB- ZSPneo Development Board

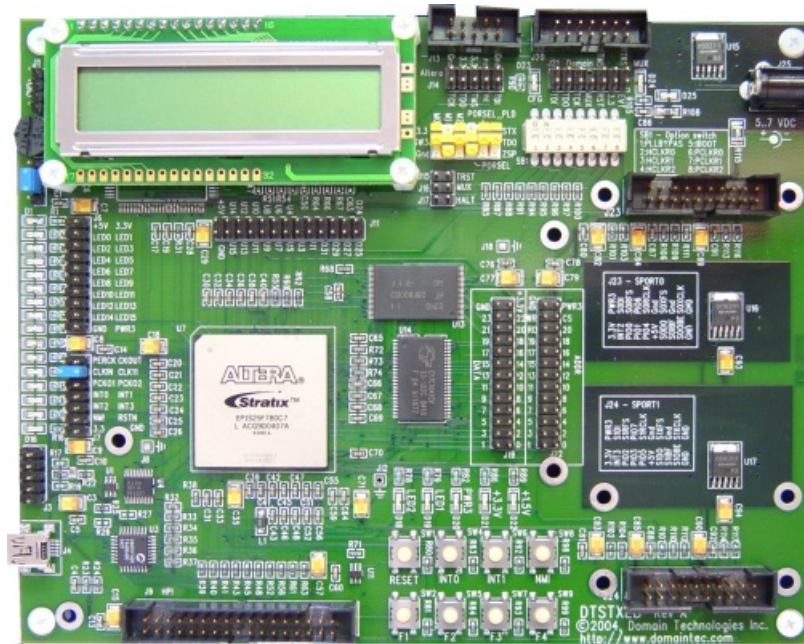
Featuring: Altera Stratix FPGA

Key Features:

- High Density Altera Stratix FPGA EP1S25F780C7
- 256Kx16 Fast Asynchronous SRAM
- 512Kx16 Flash ROM
- 8 feature configuration slide-switches
- Integrated USB controller with JTAG, 8-bit HPI
- 8 momentary-contact push button switches
- 2 20-pin serial ports with SPORT pinout
- 24.576MHz crystal clock oscillator
- 3 26-pin ports for user signals
- 16 individual LED outputs
- LCD Display
- External JTAG emulator headers

Package Contents:

- Development Board
- USB cable
- FPGA re-program software
- User's Guide with Application Notes
- VeriSilicon's ZSPneo SDK
- Universal AC/DC power (100-220V, 50-60Hz)



Overview:

Improve the efficiency of your embedded software development for the ZSPneo with the DTSTXEB. This advanced development board provides access to additional peripherals and external memory. Utilize the high density Altera Stratix FPGA to reliably prototype and test your code, thus enabling a quicker and less expensive development cycle.

Product Description:

The development board features a high density Altera Stratix EP1S25 FPGA, an integrated JTAG emulator, and an integrated host port interface. Developers can load the FPGA with:

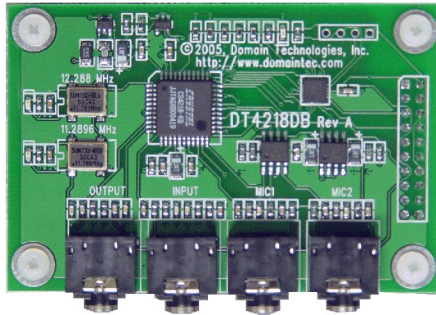
- ZSPneo Core
- Two Serial Ports
- LCD Controller
- LED Controller
- Push Button Interface
- AHB Bus
- DMA Controller
- External memory interface
- Host Port Interface

Supplemental Hardware:

This versatile development board can be integrated with additional hardware from Domain Technologies to support audio and telephony testing:

- Create a 2-line VOIP demo system by adding the DTSLIC2L board
- Create a 2 channel audio test system with stereo audio input and output by adding the DT4218DB

Testing and Debugging



DT4218DB Audio Adapter

Expand your evaluation board with the DT4218DB audio adapter daughter board. It provides stereo 16-bit codec with sampling rates

DT4218DB Features:

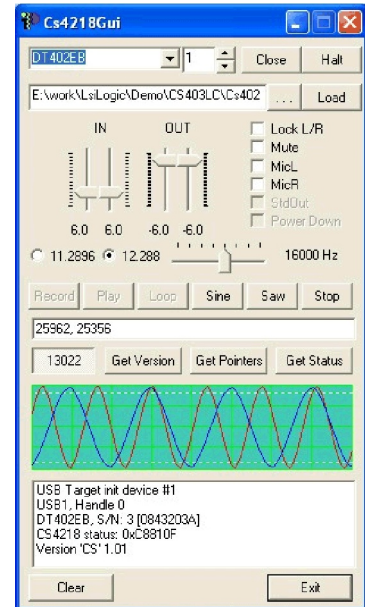
- 16-bit Stereo Audio Codec
- Delta-Sigma A/D and D/A converters
- Stereo Line Out
- Stereo Line In
- Dual MIC In

Sampling Rates:

- 48 kHz - 8 kHz
- 44.1 kHz - 7.35 kHz

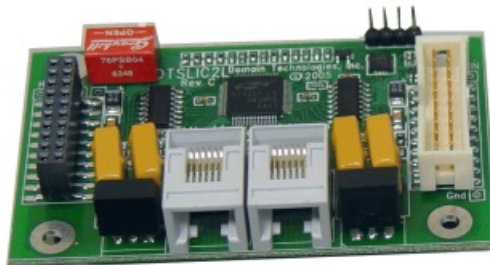
between 48kHz and 7.32 kHz including 44.1 kHz. There are two separate clock sources allowing for selection of 16 different sampling rates. In addition, the board features stereo line in and line out as well as two independent microphone inputs with preamp and bias voltage for electret microphones.

Utilize the demo software, including sources, to test the CS4218 chip on the DT4218DB audio daughterboard. The Graphical user interface of the demo monitors incoming waveforms and displays peak values for each.



DTSLIC2L Features:

- Dimensions: 3.2" x 2.5"
- 2 Powered Analog Line Interface Jacks
- 2 Independent CODECs
- DTMF, Tone Generation, Zsynth, and Line Interface
- Si3220 chip and two Si3200 chips



DTSLIC2L Telephony Adapter

Expand your evaluation board testing with the dual subscriber line interface circuit daughterboard. Each DTSLIC2L provides a complete dual analog phone line. Up to four DTSLIC2L boards can be powered by one DTSLICPWR (power supply) board.

DTSLICPWR Features:

- Power: Coiltronics VP5-0155 and LTC 3704

Utilize the demo software, including sources, to test the telephony daughterboard. The demo illustrates how to communicate with the DTSLIC2L from the PC through the ZSP host port.

