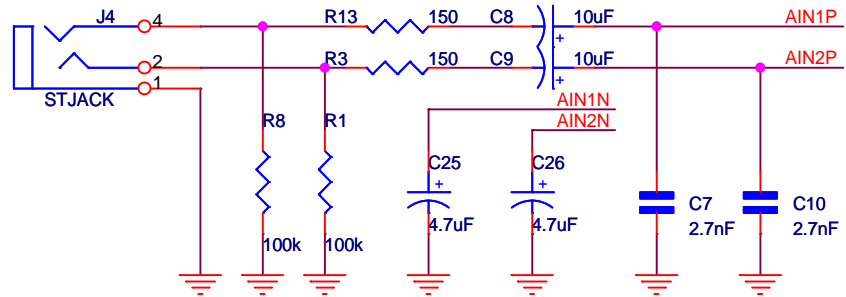


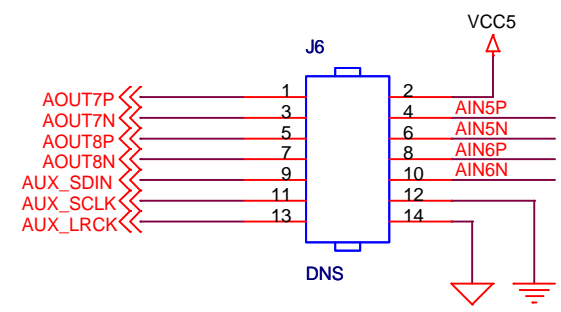
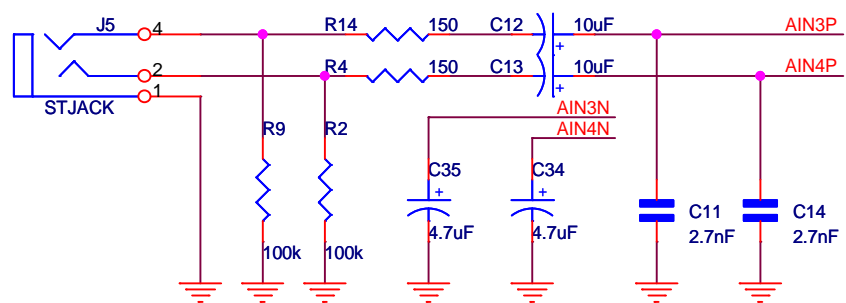
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Title		
DSP Interface		
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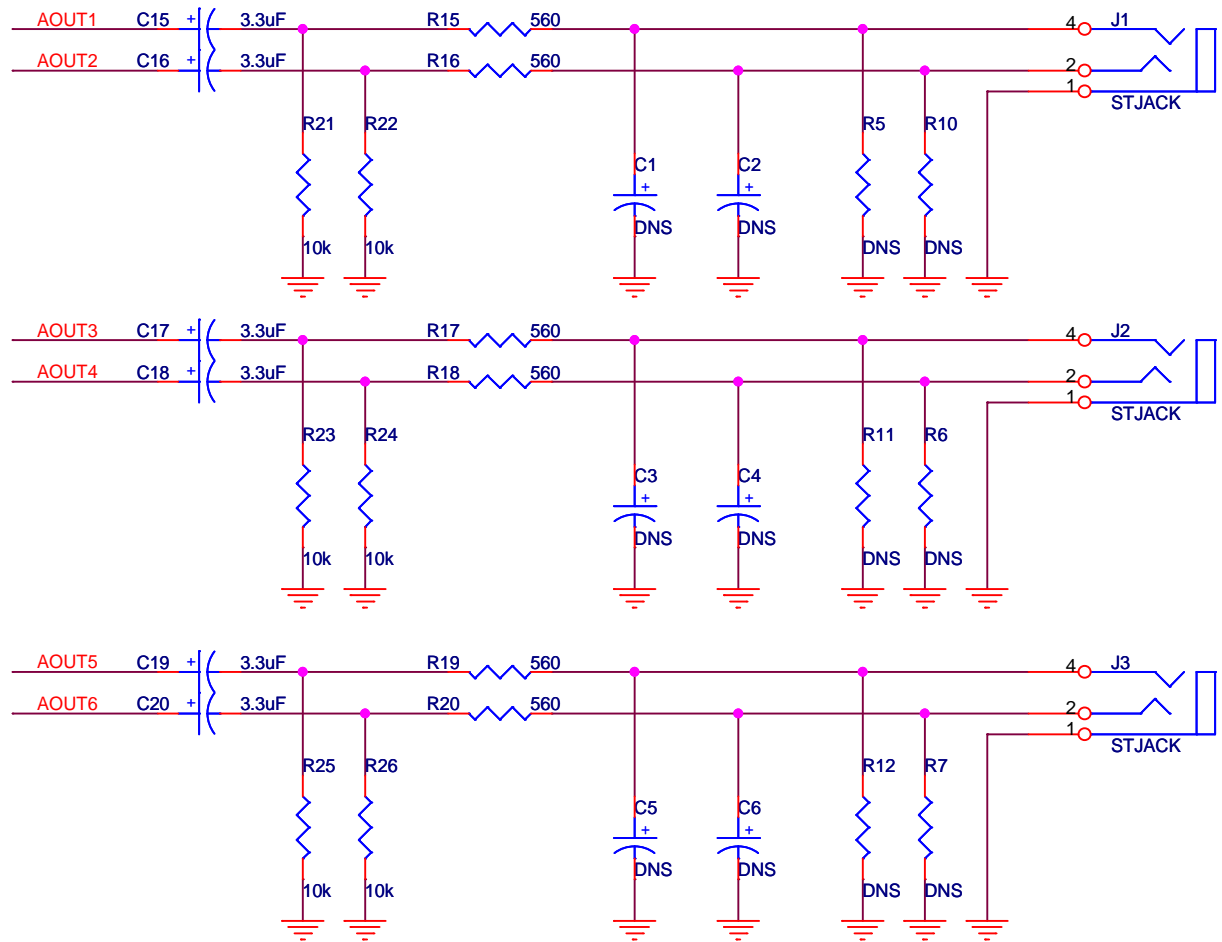
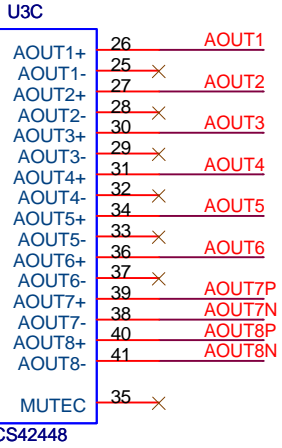
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AIN1P	46
AIN1N	45
AIN2P	48
AIN2N	47
AIN3P	50
AIN3N	49
AIN4P	52
AIN4N	51
AIN5P	58
AIN5N	57
AIN6P	60
AIN6N	59

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$$C_{ext} = \frac{R_{ext} + 560}{4 * \pi * F_s * 560}$$

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