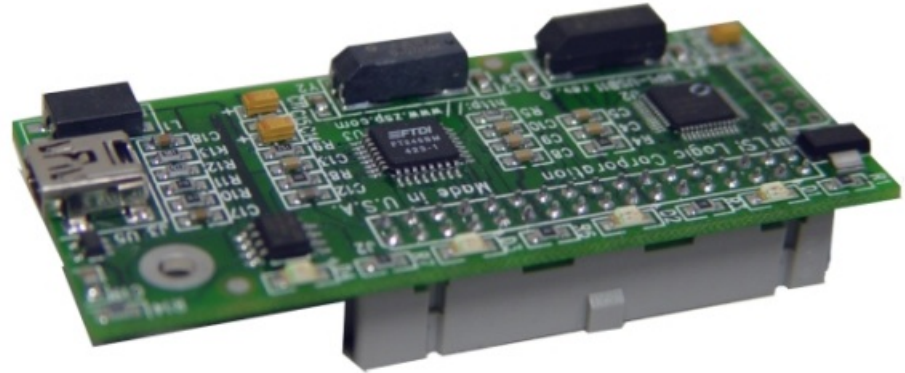


## HPI-USB

### USB1.1 Interface to ZSP Host Port



#### Key Features:

- Dimensions: 3"x1.3"
- USB1.1 with FTDI FT245
- 4 status LEDs

\*Requires Power Supply:  
50 mA @ 5-9V DC

#### Overview:

Utilize the HPI-USB daughtercard during ZSP product development. The USB to host port interface board is used for real time data transfer or control of the system.

#### Product Description:

The HPI-USB has a maximum data rate of 400KBytes per second. It's compatible with any ZSP development or evaluation board with a 34-pin HPI connector. Power is supplied by the target board. The HPI-USB can operate in either 16 or 8-bit transfer mode. The host port is monitored and serviced by the 25MHz, single cycle 8051 micro-controller. The USB data transfer is available directly through the FTDI interface DLL. A sample host application is provided. Commands and functions supported by the micro-controller are described in the comprehensive online help file: ZSPHOSTusb.chm

# Development and Debug Tools

## ZSP-USB-JTAG, SB-USB2-ZSP Probe Features:

- Powered by USB port
- JTAG interface
- Integrates with:
  - ZViewIDE
  - ZSP Console GDB



### ZSP-USB-JTAG: JTAG Probe for ZSP Debugging

Both compact and portable, the ZSP-USB-JTAG emulator is designed to support software development on the VeriSilicon ZSP cores via a host PC USB port. In addition, the emulator supports multi-core and multi-user debugging on a network over a TCP/IP connection, as well as JTAG boundary scan operations such as testing and flash memory programming, with available software.

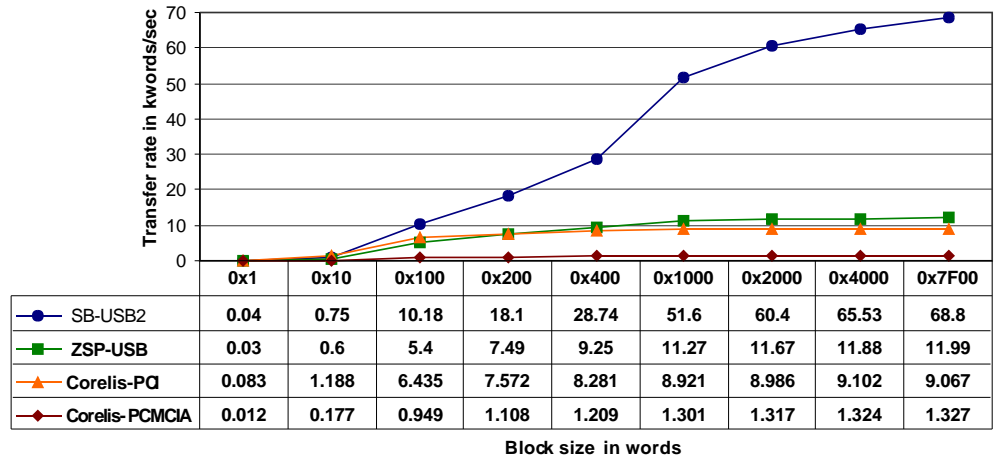
The ZSP-USB-JTAG emulator is powered from the USB connection, which eliminates the need for a separate power supply and also making it ideal for use with notebook computers.



### SB-USB2-ZSP: Fast JTAG Probe ZSP Debugging

The SB-USB2-ZSP emulator enables efficient and productive embedded software debugging. This compact and portable probe utilizes the JTAG interfaces for debug and in-circuit testing supplied with the VeriSilicon ZSP cores. Unlimited software

breakpoints within all ranges of program memory implemented in RAM are supported. Upload and download rates are up to 5 times faster than comparable products:



### ZSP-QUAD-JTAG Adapter: Daisy-chain Devices

Utilize the handy ZSP-QUAD-JTAG adapter when daisy-chaining multiple devices requiring simultaneous control. The ZViewIDE debugger allows each connected board's ZSP to run / halt / single step synchronously. In addition, non-ZSP JTAG devices can be

included in the scan chain; use ZViewIDE's boundary scan operation to observe the states of individual pins.