

DTSLIC2L- Telephony Daughterboard

Dual Subscriber - Line Interface Circuit (SLIC) Card



DTSLIC2L Features:

- Dimensions: 3.2" x 2.5"
- 2 Powered Analog Line Interface Jacks
- 2 Independent CODECs
- DTMF, Tone Generation, Zsynth, and Line Interface
- Si3220 chip and two Si3200 chips

DTSLICPWR Features:

- Power: Coiltronics VP5-0155 and LTC 3704

Overview:

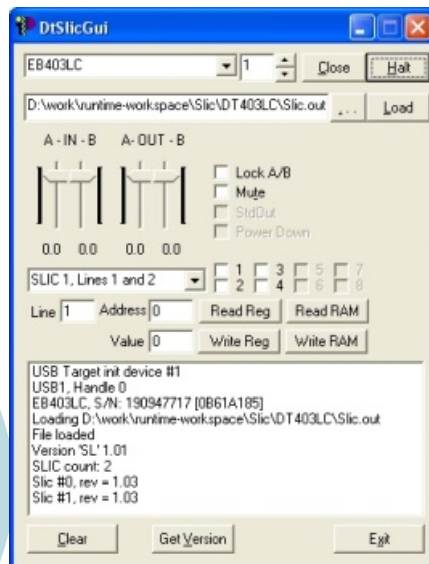
Enhance your ZSP based product development and testing using the dual subscriber- line interface circuit daughterboard. The compact DTSLIC2L features a complete dual analog phone line. Create a complete two line demonstration platform for Voice Over IP by combining the DTSLIC2L and any ZSP evaluation board. Multiple boards may be combined.

Product Description:

The DTSLIC2L contains two powered line interface jacks and a compact, economical dual-channel analog telephone interface: the SI3220-FQ Dual ProSLIC(R). The Si3220 is controlled by an 8-bit micro-controller which interfaces to the ZSP through a 2 line I²C interface. The SI3220 is fully programmable and has on-chip functions for DTMF generation/decoding, FSK caller ID generation, and modem tone detection help; in addition, its integrated test and line monitoring feature allows remote subscriber loop and line card diagnostics to be performed without centralized test equipment. Programmable internal ring generation is provided also.

Power Supply:

The DTSLICPWR board supplies power to the DTSLIC2L. Up to 4 telephony boards may be powered by one DTSLICPWR, which requires a 7.5V DC power supply.



Demo Software:

Use the DtSlicGui software, sources included to communicate with the SLIC from the PC through a ZSP host. The GUI functions with any ZSP board having a SPORT connector, such as the EB403LC or DT402EB.



Development and Debug Tools

ZSP-USB-JTAG, SB-USB2-ZSP Probe Features:

- Powered by USB port
- JTAG interface
- Integrates with:
 - ZViewIDE
 - ZSP Console GDB



ZSP-USB-JTAG: JTAG Probe for ZSP Debugging

Both compact and portable, the ZSP-USB-JTAG emulator is designed to support software development on the VeriSilicon ZSP cores via a host PC USB port. In addition, the emulator supports multi-core and multi-user debugging on a network over a TCP/IP connection, as well as JTAG boundary scan operations such as testing and flash memory programming, with available software.

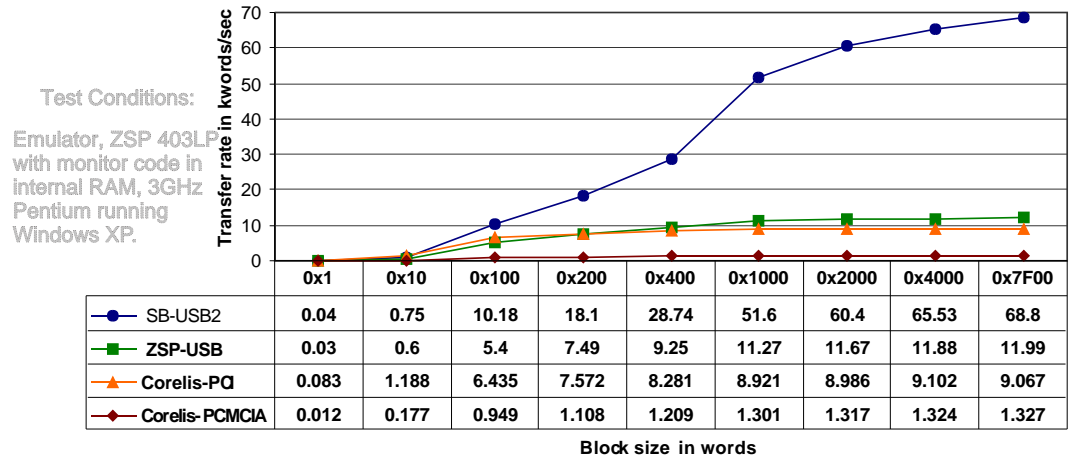
The ZSP-USB-JTAG emulator is powered from the USB connection, which eliminates the need for a separate power supply and also making it ideal for use with notebook computers.



SB-USB2-ZSP: Fast JTAG Probe ZSP Debugging

The SB-USB2-ZSP emulator enables efficient and productive embedded software debugging. This compact and portable probe utilizes the JTAG interfaces for debug and in-circuit testing supplied with the VeriSilicon ZSP cores. Unlimited software

breakpoints within all ranges of program memory implemented in RAM are supported. Upload and download rates are up to 5 times faster than comparable products:



ZSP-QUAD-JTAG Adapter: Daisy-chain Devices

Utilize the handy ZSP-QUAD-JTAG adapter when daisy-chaining multiple devices requiring simultaneous control. The ZViewIDE debugger allows each connected board's ZSP to run / halt / single step synchronously. In addition, non-ZSP JTAG devices can be

included in the scan chain; use ZViewIDE's boundary scan operation to observe the states of individual pins.